PROCEDURE TO ATTEND TEXAS INSTRUMENTS INDIA WEBENCH® DESIGN CONTEST ROUND 1

STEP 1 : Click On Start Here

TEXAS INSTRUMENTS	TI university program	STEPS
Home Eligibility Contest Structure	Start Here Help	
	Texas Instruments India WEBENCH® Design Contest	
	Email	
	Password	
	Forget Password Submit New user	

STEP 2 : Enter Your Email and Password and then click Submit

STEP 3 : Round 1 Instructions page displayed



STEP 4 : Read the Instructions carefully and click Take a test



1. Select the value of average rectified Vin when Vin is 170 V and 240 V ?

STEP 6 : Click Link to WEBENCH® to workout

🔱 Texas Instruments	Everything ~ Search			Q			
Products Applications & designs Tools 8	software Support & community	Sample & buy	About TI	🕙 History 🍃	Cart 🧲	🕀 Engl	
My products ★ × Motor Drive BoosterPack featuring DRV830' ★ × SimpleLink Wi-FI CC3100 BoosterPack: Buy N	l and NexFET Buy Now Now	My technical o ★ × LM502 PWM Co ★ × Advan technology	locuments 23 AC-DC Quasi-R icing the smart fa	esonant Current Mode	My searcl × E × [i hes EK-TM4 DRV830	
TI Home > WEBENCH® Design Center WEBENCH® Design Cen	Boost your skills: Get 20% off	motor driver Booste	rPacks today! >				
WEBENCH Design Environments are unique and powerful software tools that deliver customized power, lighting, filtering, clocking and sensing designs in seconds. These easy-to-t tools help you generate, optimize and simulate designs that conform to your unique specifications. They allow you to make value-based tradeoffs at a design, system and supply chain level before your design is committed to production.		asy-to-use	Se WEBENCH® Designer MyDesigns Filters Sensors Interface Reference Power FPGA/µP LED Clocks Enter your power supply requirements: @ nr @ er				

STEP 7 : Enter the user Requirement parameters and click Start Design

My Designs WEBENCH® Designer Sensors Interface Reference Filters FPGA/µP LED Clocks Power Enter your power supply requir nents: ODC OAC AC Frequency 50Hz • 60Hz Min Max Vin 100 V 240 RMS V Vout lout 19 V 3.4 Output А 30 Ambient Temp °C Multiple Loads Single Output **Power Architect** Start Design

STEP 8 : VISUALIZER Screen opens as shown below and click the Part LM 5023 to download the Data sheet

My Designs/Projects		Rew Solutions Visualizer Assistant		усский Язык Português De	- utsch We	icome vsr@:	stepsmail.com 🔹 🕖
		VISUALIZER					8
	WEBENCH® Optimizer	Change Inputs	Ac	Ivanced Filters			
	Lowest BOM Cost Footprint 2172 NA 84%	DC • AC Freq: 50 • 60 Hz Vin RMS Min: 100 V Vin RMS Max: 240 Vout: 19 V Iout: 3.4 Amb. Temp: 30 Use Advanced Options >> Recalculate Image: 100 minipage	C Enable Pin Power Good A Jutomotive Soft Start Ext Sync Light Load Sync Switch Feature Filters	Efficiency (>=): 84% 88	5% m ² \$0 All		
Advanced Charting			Solutions				
Efficiency V Footprint V BOM C	Cost Part C	reate WEBENCH® Tools Schen	atic BOM Images	Design BOM Considerations Footprint (mm2)	BOM Eff Cost (%)	BOM Count (Freq Vout p- Xov kHz) p (mV) Fre
2240 - Feo 2220 - 2180 - 21	LM5023 Oper	i Design	2172mm ²	AC-DC QR Current Mode 2172 PWM Controller	NA 84%	48	124 57.87 N/
MOSt 84 86 88 99 92 94 Efficiency Efficiency vs. Footprint vs. BOM Cost	96 98 100						

STEP 9 : Open Design (you need to use your my.ti.com username and password) and the following screen WEBENCH



Summary screen appears.

STEP 10 : Answer the questions using various menus such as OP Values, BOM Charts and Data sheet then press submit

for submitting the results

STEP 11 : The below window appears







Texas Instruments India WEBENCH[®] Design Contest

Congratulation !!

Proceed to Round 2